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Application Number	09/779,749
Filing Date	February-09, 2001
First Named Inventor	Mehrdad Abrishami
Group Art Unit	2661
Examiner Name	
Attorney Docket Number	079257/0103 / PV-004

JAN 04 2002

I hereby appoint:

 Practitioners at Customer Number -

OR

 Practitioner(s) named below:

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RALPH VESELI	33,807
SANDEEP JAGGI	43,331
GARY B. GOATES	35,159
LEO J. PETERS	33,562
TIMOTHY R. CROLL	36,771



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PATENT TRADEMARK OFFICE

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<input checked="" type="checkbox"/> Firm or Individual Name	LSI LOGIC CORPORATION				
Address	1551 McCarthy Blvd., MS: D-106				
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I am the:

 Applicant Assignee of Record of the entire intere

Certificate under 37 CFR 3.73(b) is enclose

SIGNATURE of Applicant or Assignee of Record

Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp	
Signature		
Date	11-15-01	

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Technology Center 2600

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mehrdad Abrishami et. al.

Serial No.: 09/779,749

Group Art Un 2661

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Filed: February 09, 2001

Technology Center 2600

For: Fax Transmission Over Congested
Or Corrupted Wideband Network,
Or Narrowband Network, Using
ECM Error Block Flow Control

Atty Docket: 079257/0103 / PV-004

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Connie Del Castillo

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CERTIFICATION UNDER 37 C.F.R. 3.73(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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Date: 11/15/01

Respectfully submitted,

SJ

Sandeep Jaggi
Chief IP Counsel
Corporate Assistant Secretary
LSI LOGIC CORPORATION